

10

15a
SENSOR
MODULE

15b
SENSOR
MODULE

14
INTERNET

11
CENTRAL
COMPUTER

12
DISPLAY
MONITOR

13
RECEIVER

15e
SENSOR
MODULE

15c
SENSOR
MODULE

15d
SENSOR
MODULE

Figure 1

FIG. 2 is a block diagram of the system of FIG. 1, showing the interconnection of the various components.

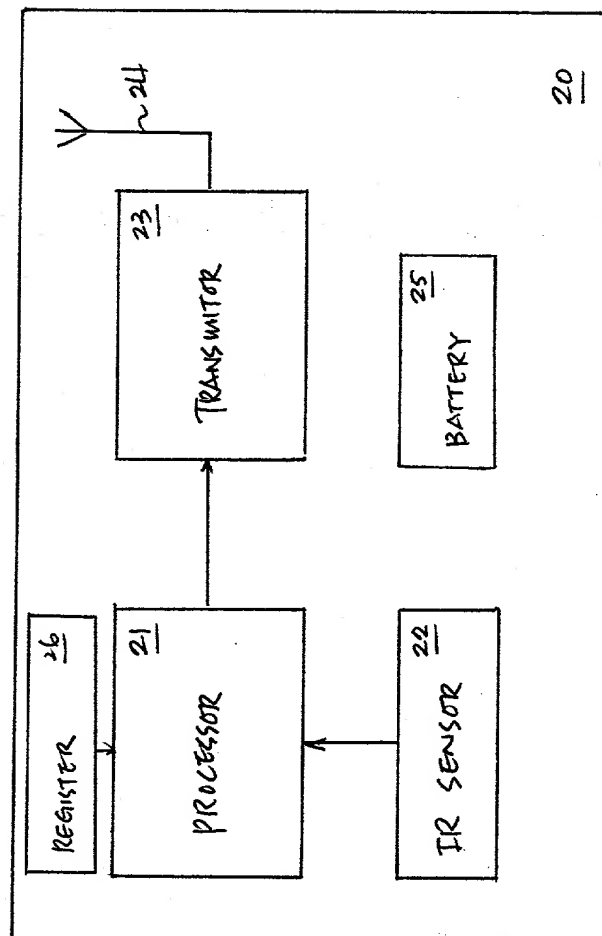


FIGURE 2

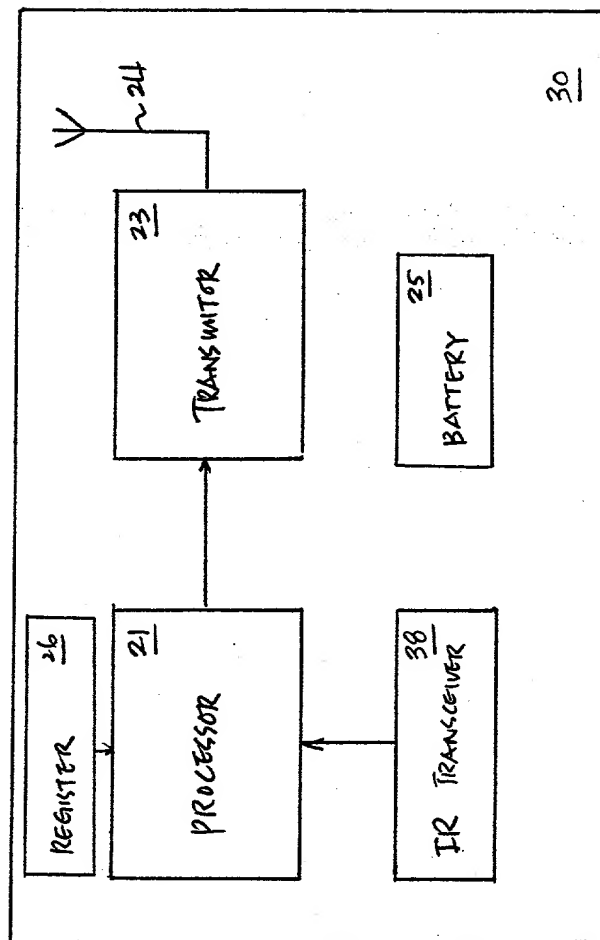


FIGURE 3

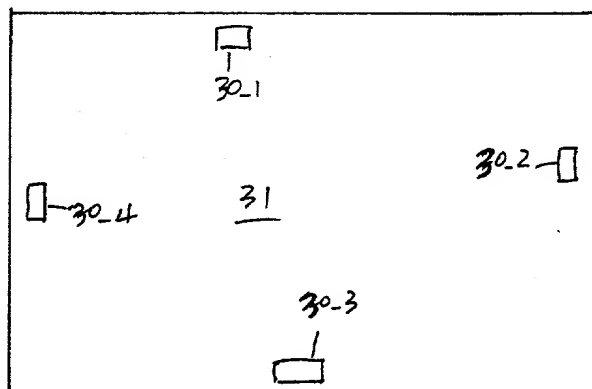


FIGURE 3A

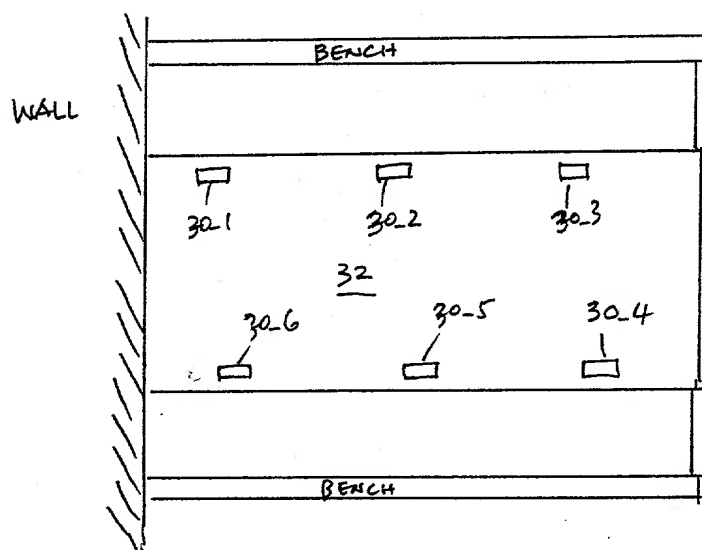


FIGURE 3B

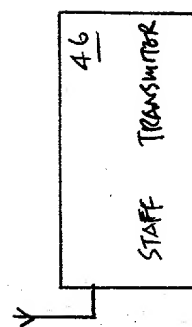
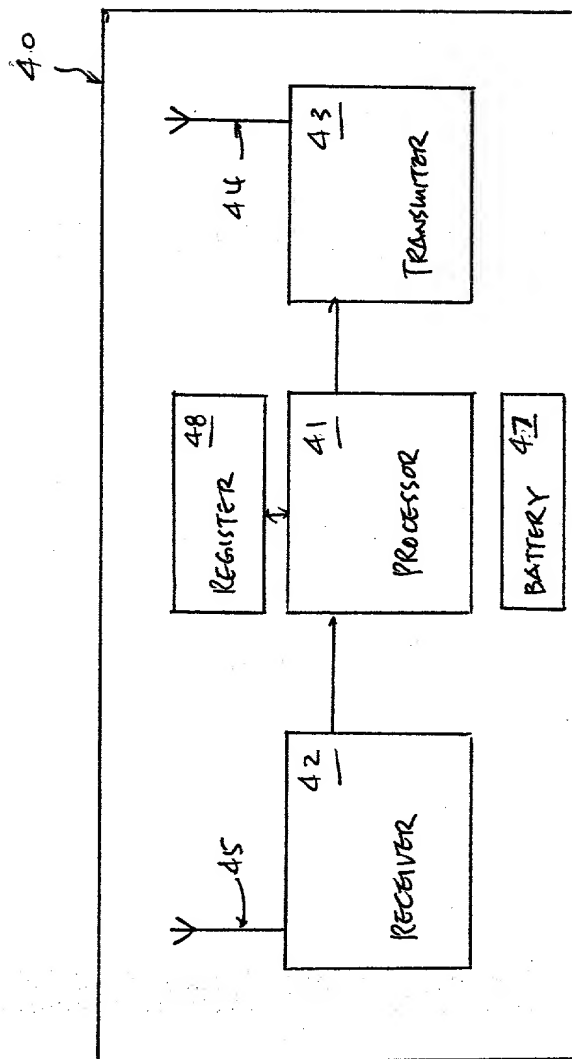


Figure 4

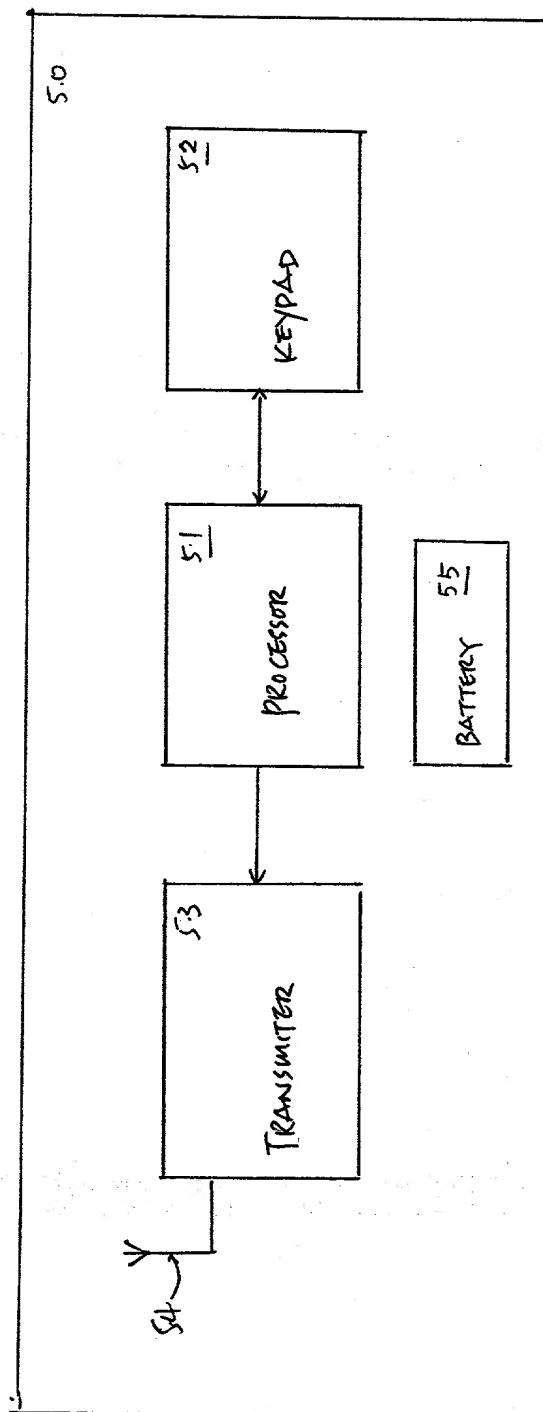


FIGURE 5

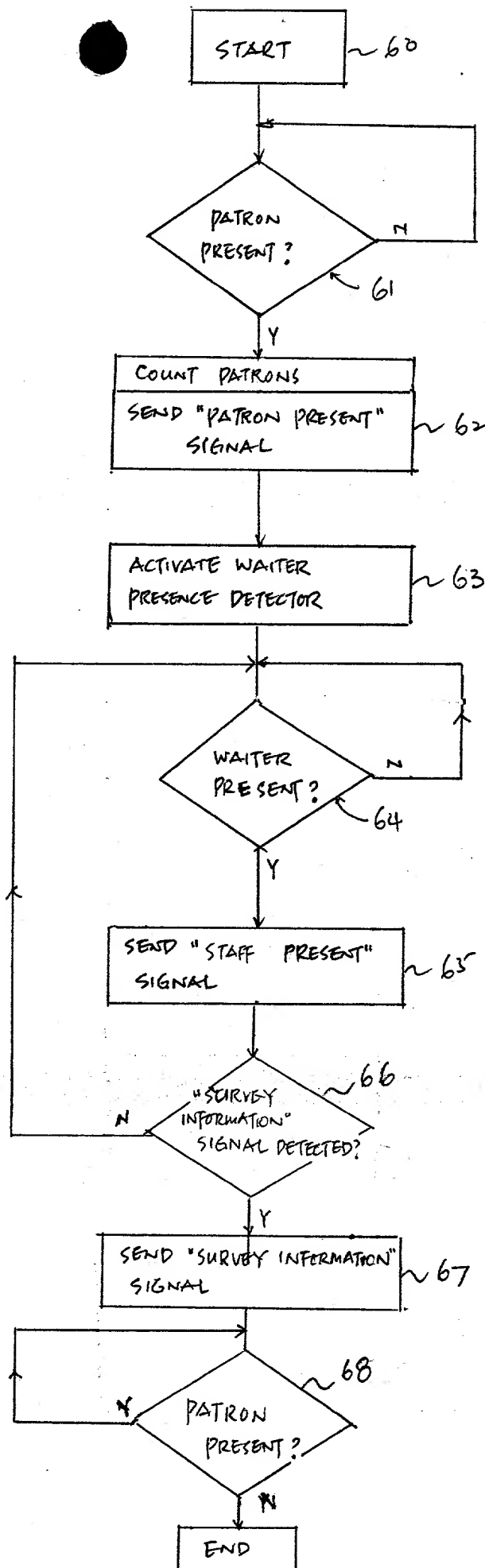


FIGURE 6